

TIMETABLE OF THE 1st SEMESTER

TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY
16:00-16:50	FPGA	MCM	EAV	SUBJECT PRESENTATIONS (Weeks W1 and W2) THURSDAY CLASSES (Weeks W4 to W14) see table below
17:10-18:00	ESC	MCC	EEG	
18:10-19:00	ADM	ASD	HFEC	
19:10-20:00				

THURSDAY ACTIVITIES DISTRIBUTION

TIMETABLE	W1	W2	W3	W4	W5	W6	W7	W8	W9	W10	W11	W12	W13	W14	
16:00-16:50	FPGA	EEG		FPGA	EEG	EAV	MCC	MCM	EEG	HFEC		EAV	HFEC	MCC	
17:10-18:00	ESC	MCC		ESC	HFEC	ASD	HFEC	ADM	FPGA	ASD		ADM	MCM	ESC	
18:10-19:00	EAV	MCM													
19:10-20:00	HFEC	ASD													
20:10-21:00		ADM													

EEG	Electrical Energy Generation	FPGA	Design of Digital Systems with FPGA
ESC	Energy Storage and Conversion	HFEC	High Frequency Electronics and Communications
EAV	Electrical Architecture of Vehicles	ASD	Automotive Software Development
MCC	Modelling and Control of Switching Converters	ADM	Automotive Development Methodologies
MCM	Modelling and Control of Electric Motors		

TIMETABLE OF THE 2nd SEMESTER

TIME	MONDAY	TUESDAY	WEDNESDAY	THURSDAY
16:00-16:50				On Site PEL and ESCL LABORATORIES (Weeks W1 TO W16) Labs 101, 103, 108 and 110
17:10-18:00			INIT (Online)	
18:10-19:00				
19:10-20:00				

PEL and ESCL LABORATORIES TIMETABLE

TIMETABLE	W1	W2	W3	W4	W6	W7	W8	W9	W10	W11	W12	W13	W14	W15	W16															
SESSIONS	S1	S2				S3				S4				S5				S6				S7				S8				
TIME \ GROUP	A	B	A	B	A	B	A	B	A	B	A	B	A	B	A	B	A	B	A	B	A	B	A	B	A	B	A	B		
16:00-16:50	L 1	L 1																												
17:10-18:00	0 3	0 8	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1	L 1
18:10-19:00	L 1	L 1	0 3	0 8	0 8	0 3	1 0	0 8	0 8	1 0	1 0	1 8	0 8	1 8	0 0	0 8	0 0	1 0	1 0	0 8	0 8	0 0	1 0	1 8	0 8	0 0	1 0	1 8	0 8	0 0
19:10-20:00	0 8	0 3					1			1	1																			



Classes on Friday (only in 2024)

PEL	Power Electronics Laboratory(On site 1 session every 2 weeks)
ESCL	Embeded System and Communications Laboratory (On site 1 session every 2 weeks)
INIT	Innovation in Industrial Technologies (Online)